Substitute for form 1449A/PTO

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Sheet	1	of	1

Complete if Known	
Application Number	10/651,523
Filing Date	August 29, 2003
First Named Inventor	Steven K. Reinhardt
Art Unit	Not Assigned
Examiner Name	Not Assigned
Attorney Docket Number	42P15451

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
ML	1	HAITHAM AKKARY, MICHAEL A. DRISCOLL, "A Dynamic Multithreading Processor," Proceedings of the 31st Annual International Symposium on Microarchitecture, Nov. 30-Dec. 2, 1998, pp. 1-11,	
ML		Dallas, Texas, USA.	
ML	2	SEON WOOK KIM, ET AL., "Reference Idempotency Analysis: A Framework for Optimizing Speculative Execution," Proceedings of the SIGPLAN Symposium on Principals and Practice of Parallel Programming (PPoPP),	
ML		18-20 June 2001, pp. 1-10, Snowbird, Utah, USA.	
ML	3	DEBORAH T. MARR, ET AL., "Hyper-Threading Technology Architecture and Microarchitecture," Intel Technology Journal Q1, 2002, pp. 1-12.	
ML	4	SHUBHENDU S. MUKHERJEE, ET AL., "Detailed Design and Evaluation of Redundant Multithreading Alternatives," 29th Annual International Symposium on Computer Architecture (ISCA), 2002, pp. 1-12.	
ML	5	STEVEN K. REINHARDT, SHUBHENDU S. MUKHERJEE, "Transient Fault Detection via Simultaneous Multithreading," 27th Annual International Symposium on Computer Architecture, June 2000, pp. 1-12.	

Examiner Signature	/Dieu-minh Le/	Date Considered	07/26/2006

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.